CONTINUOUS INTERNAL EVALUATION– 3

Dept: EC	Sem: **3EC A & B**	Course: **Computer Organization and Architecture**	Course Code: **18EC35**
Date: 17.02.2021	Time: 9:30-11.00am	Max Marks: 50	Elective: N

Note: Answer any 2 full questions, choosing one full question from each part.

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QN		Questions	Marks	RBT	COs
		PART A			
1	a	Briefly explain different types of non-volatile memory	8	L2	CO4
	b	With a neat diagram explain virtual memory organization	8	L2	CO4
	c	Draw and explain three bus organization of CPU	9	L2	CO5
		OR			
2	a	Describe the working principle of magnetic disk	8	L2	CO4
	b	With diagram explain the memory hierarchy system	8	L2	CO4
	c	Describe the sequence of control signals to be generated to fetch an instruction from memory in a single bus organization	9	L2	CO5
		PART B			
3	a	With neat diagram explain the concept of cache memory	8	L2	CO4
	b	Discuss the organization of hardwired control unit	8	L2	CO5
	c	Write down the control sequence for execution of instruction ADD (R3), R1	9	L2	CO5
		OR			
4	a	Write a note on: i) Cache Hit ii) Cache miss iii) Floppy Disk iv) Operating System	8	L2	CO4
	b	With example explain single bus organization	8	L2	CO5
	c	What is micro-instruction? With diagram explain basic organization of micro-programmed control unit	9	L2	CO5

Prepared by: Suhandas   HOD